

Characterization and modeling of bias dependent breakdown and self-heating in GaInP/GaAs power HBT to improve high power amplifier design

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It is usual to say that power GaInP/GaAs heterojunction bipolar transistors (HBTs) have many advantages for power amplification at microwave frequencies, because of their high gain and high power density. Furthermore, the possibility of controlling the base biasing conditions (voltage, current, self-bias control) compared to a field-effect transistor offers additive degrees of freedom to make a tradeoff between linearity and power-added efficiency. Nevertheless existing devices are limited because of the relatively low breakdown voltage whereas high collector voltage swings are required to achieve high power. This drawback makes them not appropriate for use in the next generation of mobile communication base station or radar systems. Silicon technologies such as LDMOS and III-V devices (MESFET and HFET) present competitive performances in term of high power level but for medium power added efficiency. Important improvements have been made in recent years which make possible large breakdown voltages for GaInP/GaAs HBTs. Breakdown value close to 67 V has been achieved. The aim of this work is to significantly improve the modeling of the breakdown voltage on this type of transistor. Furthermore, the in depth characterization and modeling of self-heating effects have been greatly improved in order to improve thermal management solutions which enable us to enhanced design solutions of HBT high power amplifiers.

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